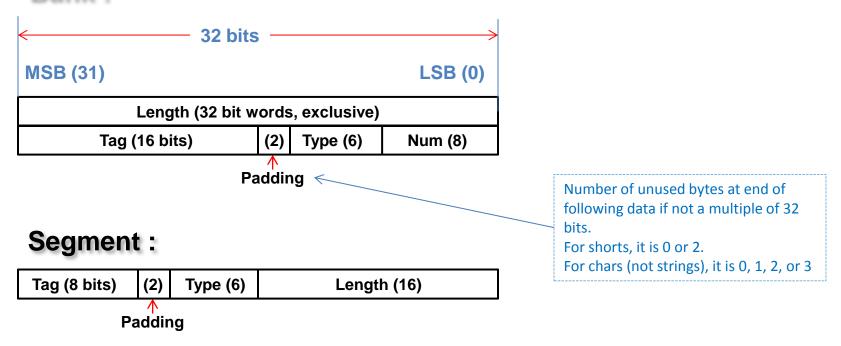
CODA Online Data Formats

Evio Header Formats

Bank:



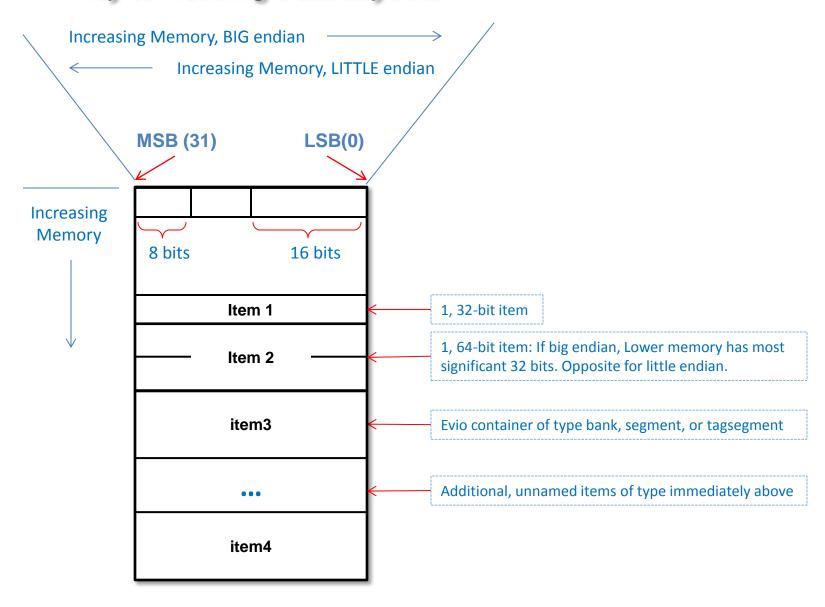
Tag Segment:

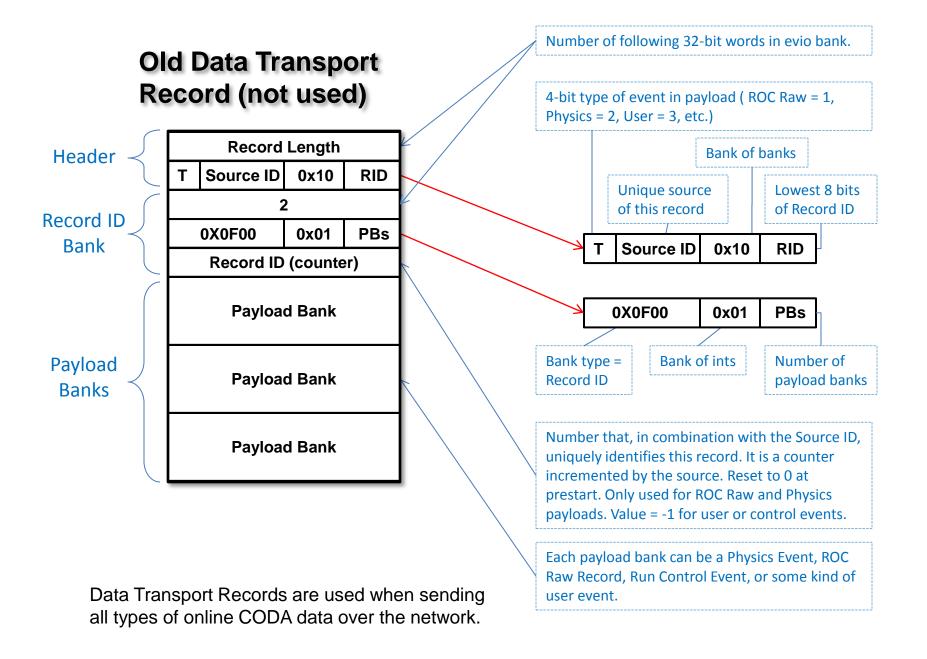
| Tag (12 bits) | Type (4) | Length (16) |
|---------------|----------|-------------|

Evio Content Type Codes

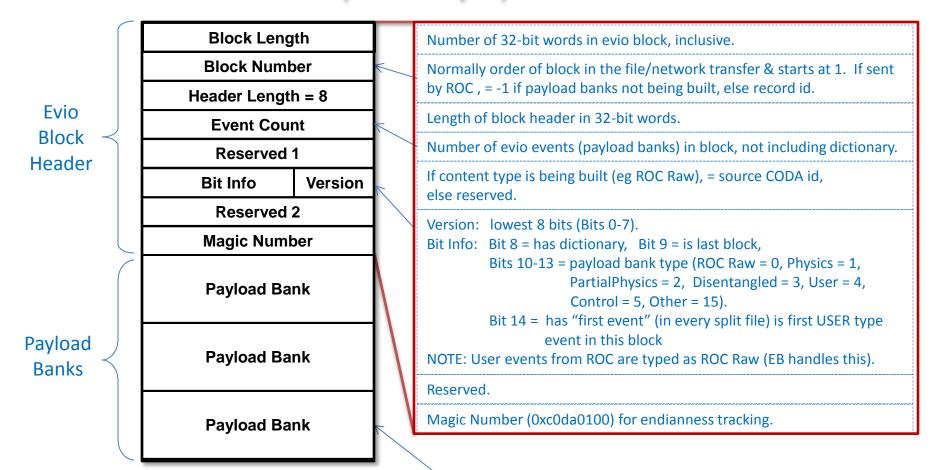
| Content Type | Primitive Data Type | |
|--------------|--|--|
| 0x0 | 32 bit unknown (not swapped) | |
| 0x1 | 32 bit unsigned int | |
| 0x2 | 32 bit float | |
| 0x3 | 8 bit char* (string) | |
| 0x4 | 16 bit signed int | |
| 0x5 | 16 bit unsigned int | |
| 0x6 | 8 bit signed int | |
| 0x7 | 8 bit unsigned int | |
| 0x8 | 64 bit double | |
| 0x9 | 64 bit signed int | |
| Оха | 64 bit unsigned int | |
| 0xb | 32 bit signed int | |
| Охс | Tag Segment | |
| 0xd | Segment | |
| 0хе | Bank | |
| 0xf | Composite | |
| 0x10 | Bank | |
| 0x20 | Segment | |
| 0x21 | Hollerit (only used internally for Composite data) | |
| 0x22 | N value (only used internally for Composite data) | |

Key to Reading Data Layouts





Network Transfer (Evio Output) Format



Format used when sending all types of online CODA data over the network. They are in standard evio buffer/file output format with block headers.

Each payload bank can be a Physics Event, ROC Raw Record, Control Event, or User event. Note: there may be a block header between any 2 payload banks.



Control Event Length = 4

Event Type 0x01 0

time

A

В

Bank of uints

Event Type

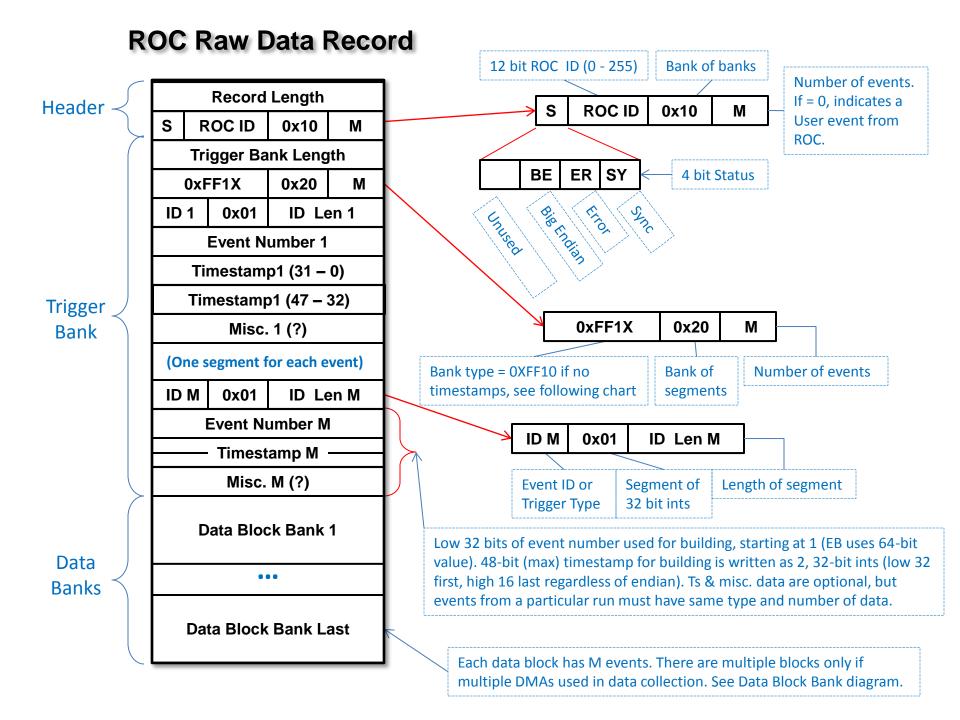
0x01

0

16 bit Control event type:

- 0xFFD0, Sync
- 0xFFD1, Prestart
- 0xFFD2, Go
- 0xFFD3, Pause
- 0xFFD4, End

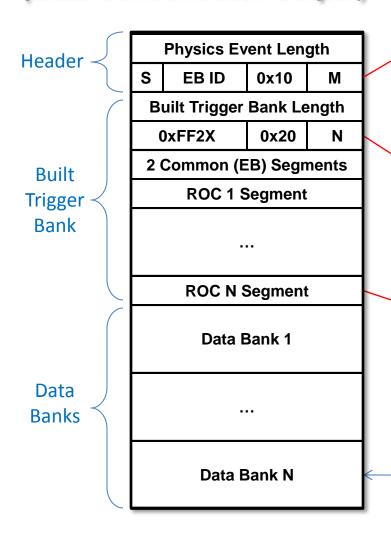
| Event Type | Α | В |
|-------------------|--------------------------|------------------------|
| Sync | # events since last sync | # events in run |
| Prestart | run number | run type |
| Go | (reserved) | # events in run so far |
| Pause | (reserved) | # events in run so far |
| End | (reserved) | # events in run so far |

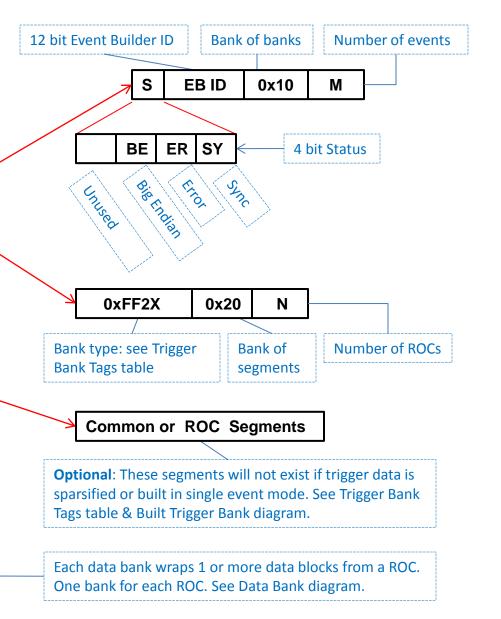


TRIGGER BANK TAGS

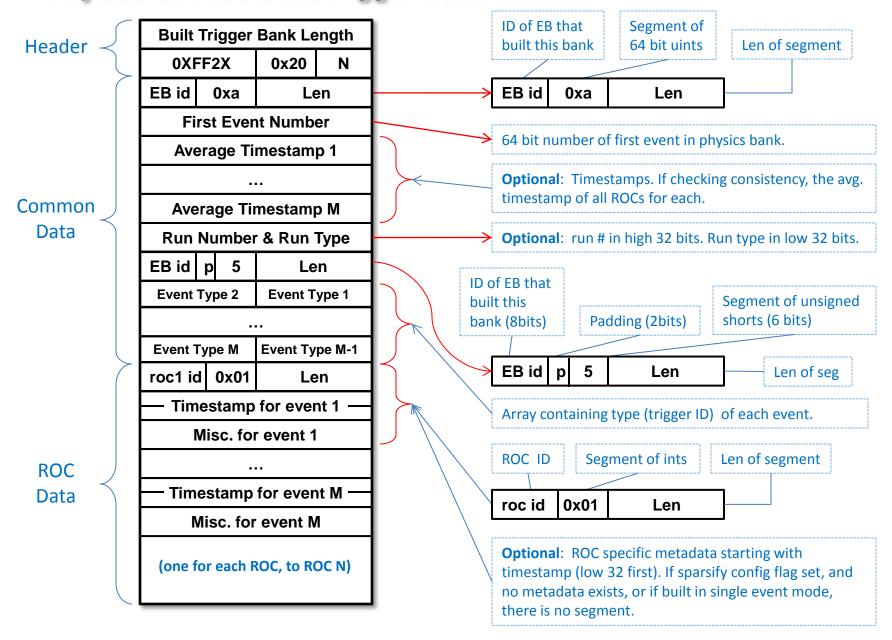
| Tag Value | Purpose |
|-----------|--|
| 0xFF10 | Raw trigger, no timestamps |
| 0xFF11 | Raw trigger, w/ timestamps |
| 0xFF20 | Built trigger, no timestamps, no run # & run type, includes run specific data |
| 0xFF21 | Built trigger, w/ timestamps, but no run # & run type, includes run specific data |
| 0xFF22 | Built trigger w/ run # & run type, but no timestamps, includes run specific data |
| 0xFF23 | Built trigger with timestamps and run # & run type, includes run specific data |
| 0xFF24 | Built trigger, no timestamps, no run # & run type, no run specific data |
| 0xFF25 | Built trigger, w/ timestamps, but no run # & run type, no run specific data |
| 0xFF26 | Built trigger w/ run # & run type, but no timestamps, no run specific data |
| 0xFF27 | Built trigger with timestamps and run # & run type, no run specific data |

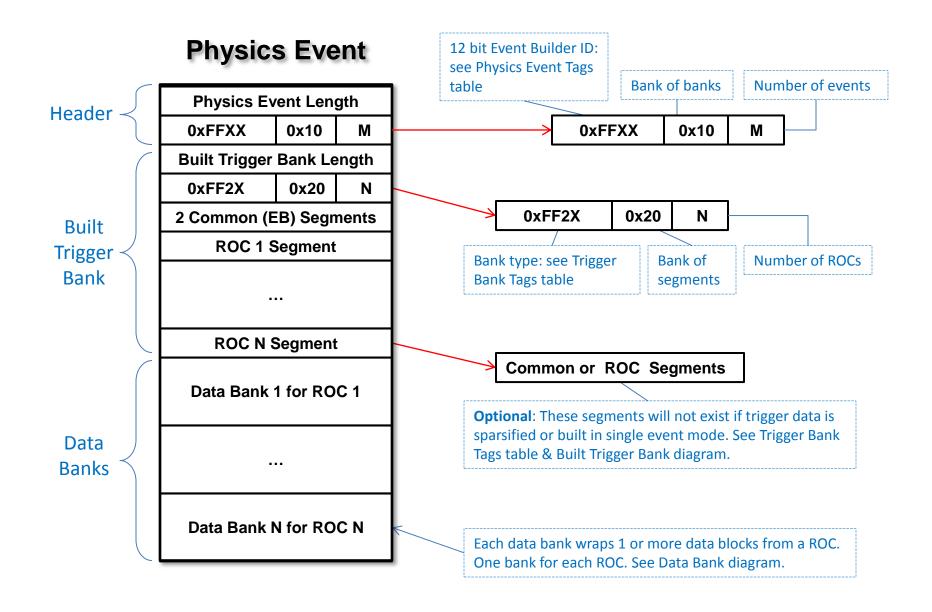
Partially-Built Physics Event (Data Concentrator Output)





Physics Event's Built Trigger Bank





CONTROL EVENT TAGS

CODA RESERVED BANK TAGS

| Tag Value Range | Purpose |
|-----------------|-----------------------------------|
| 0xFF00 - 0xFFFF | Complete range of reserved values |
| OxFFEO - OxFFFF | Undetermined |
| 0xFFD0 - 0xFFDF | Control events |
| 0xFF90 - 0xFFCF | Undetermined |
| 0xFF50 - 0xFF8F | Physics events |
| 0xFF10 - 0xFF4F | Trigger banks |
| 0xFF00 - 0xFF0F | Undetermined |

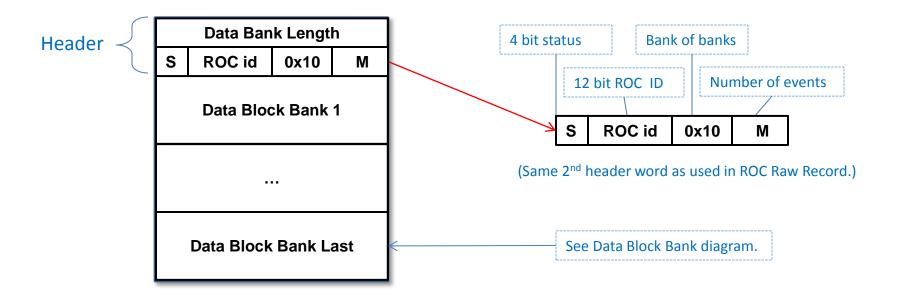
| Tag Value | Control Event |
|-----------|----------------------|
| 0xFFD0 | Sync |
| 0xFFD1 | Prestart |
| 0xFFD2 | Go |
| 0xFFD3 | Pause |
| 0xFFD4 | End |

PHYSICS EVENT TAGS

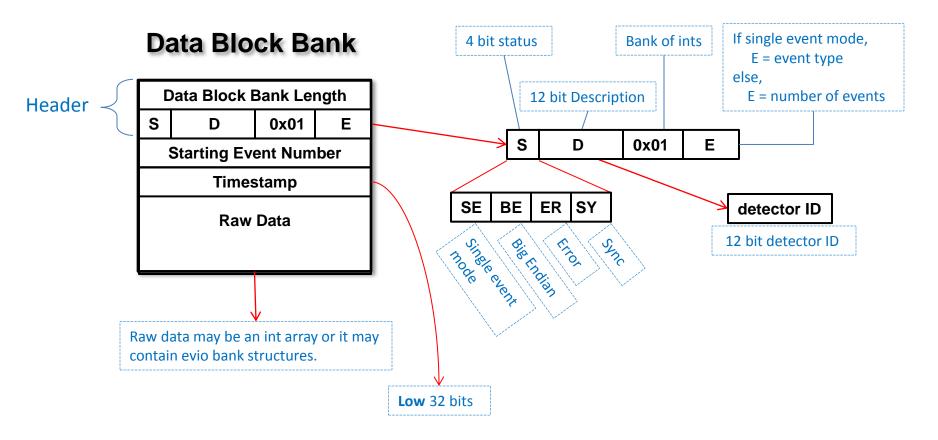
| Tag Value | Event Made by |
|-----------|-------------------|
| 0xFF50 | PEB |
| 0xFF58 | PEB with sync set |
| 0xFF70 | SEB |
| 0xFF78 | SEB with sync set |

4th bit set indicates that the last event in the entangled block is a sync event

Physics Event's Data Bank

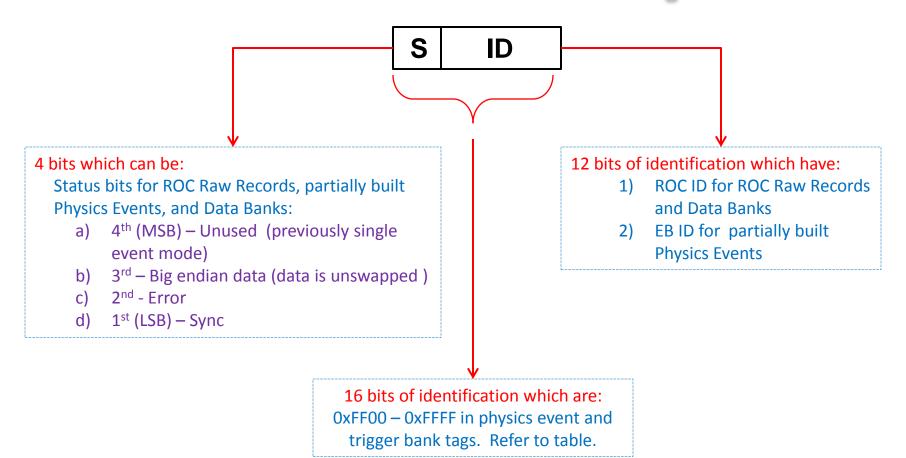


Data blocks from a single ROC are wrapped in this data bank. There should be at least one data block and there may be more if more than one DMA is used in acquiring data for this ROC. If more than one block, each contains a fragment for every one of the M events and from unique modules. In addition, the last block may have data associated only with the last event (such as scalar data).

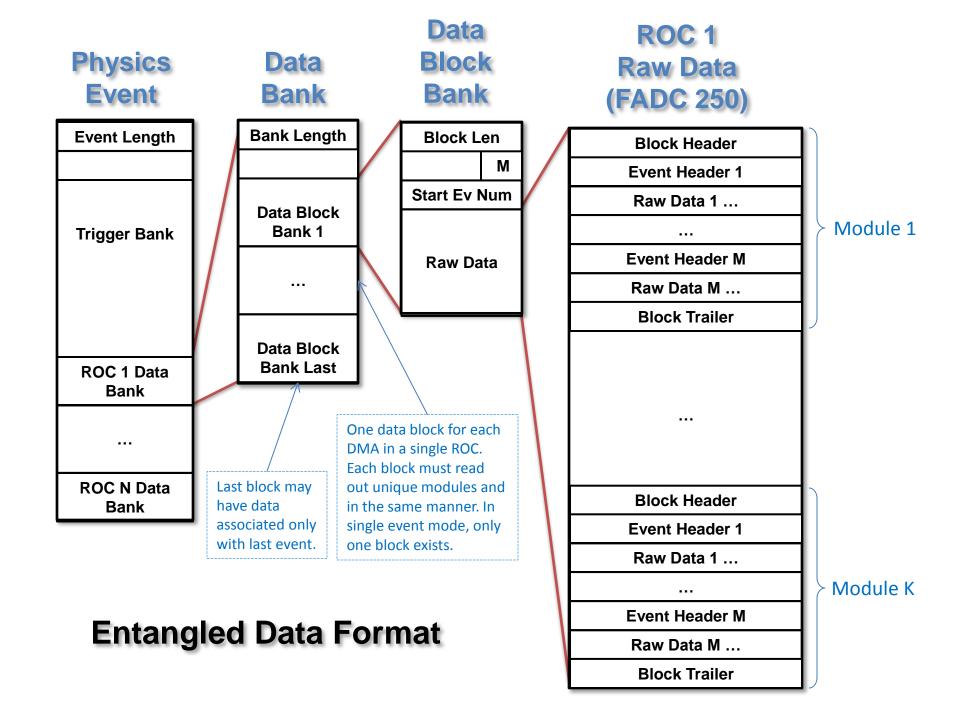


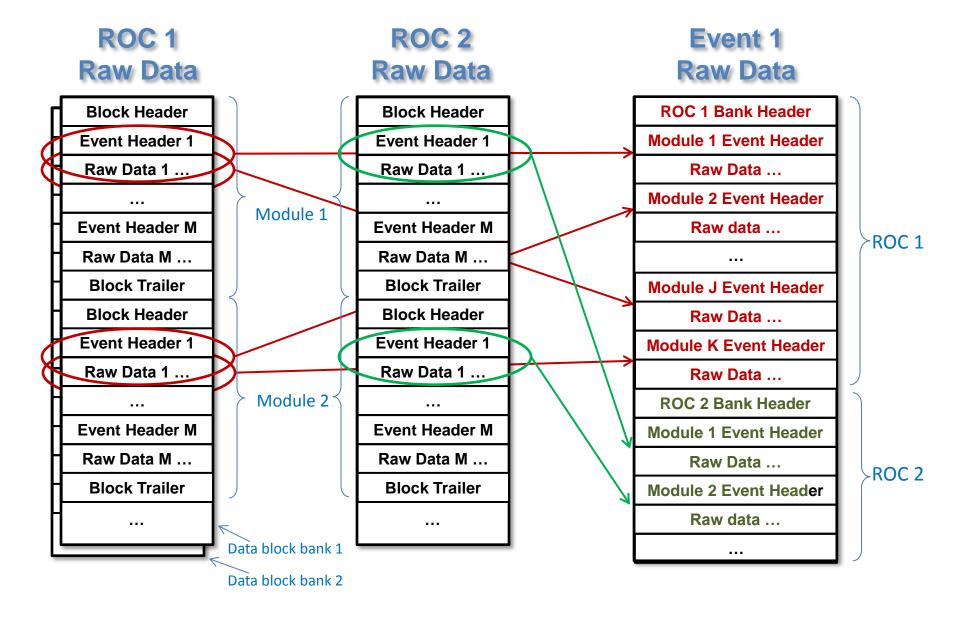
Contains raw data from a single ROC containing one or more events. If this block is the last in a data bank, and there are multiple events, and E = 1, then this data is associated only with the last event (e.g. scalar readout).

16-bit EVIO CODA-Format Tag



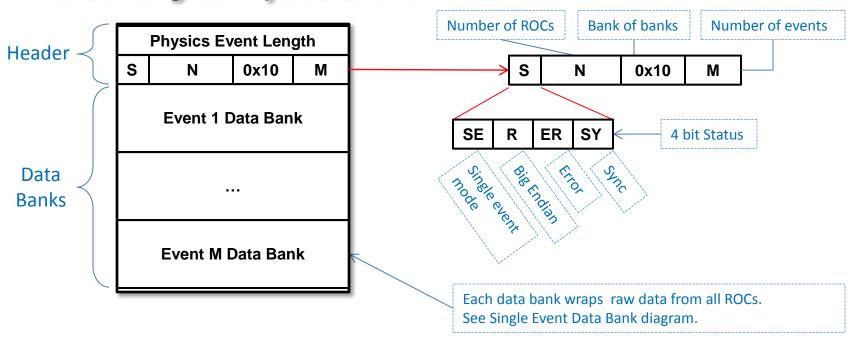
Disentangling Built Physics Event



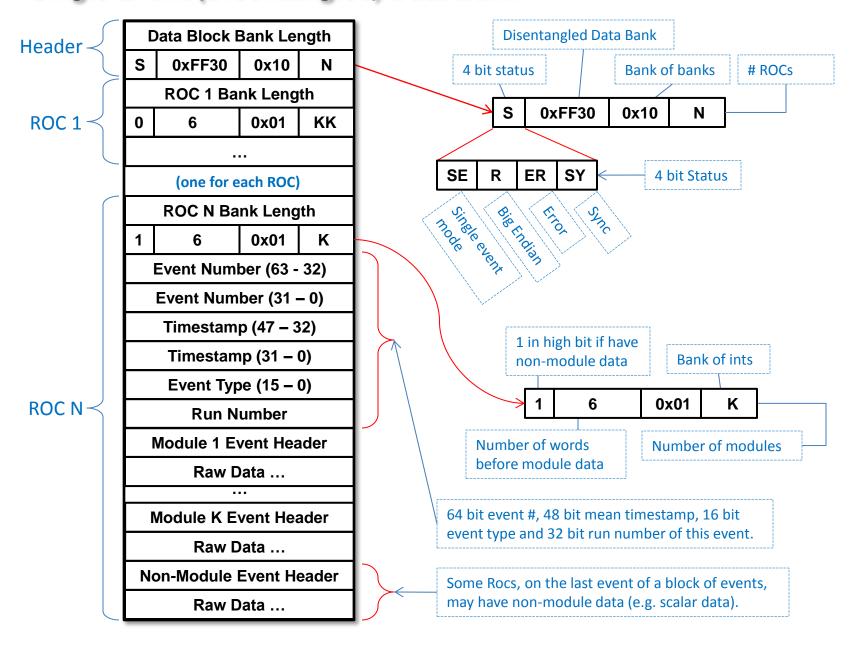


Entangled To Disentangled FADC 250 Raw Data

Disentangled Physics Event



Single Event (Disentangled) Data Bank



FADC 250

Data Type Values

0 – block header 1 – block trailer 2 – event header 3 – trigger time

4 – window raw data

5 – window sum

6 – pulse raw data

7 – pulse integral

8 - pulse time

9 – streaming raw data

10 - 12 user defined

13 – event trailer (debug only)

14 – data not valid (empty module)

15 - filler (non-data) word

Block Header Word Format

| Bits | Value | Comment |
|---------|---------------|--|
| 31 | 1 | This is a type defining word |
| 30 – 27 | 0 | Data type = block header |
| 26 – 22 | Slot ID | Set by VME64 backplane |
| 21 – 14 | Event # | Number of events in block |
| 13 – 12 | Module Type | 0=FADC250, etc. |
| 11-0 | Event block # | Used to align block when building events |

General Data Word Format

| 31st bit | Bits | Usage |
|----------|---------|---|
| 1 | 30 - 27 | 4-bit data type (see chart) |
| 1 | 26 - 0 | Data type dependent data payload |
| 0 | 30-0 | Data payload using last defined data type |

Block Trailer Word Format

| Bits | Value | Comment |
|---------|-------------------------------------|---------------------------------|
| 31 | 1 | This is a type defining word |
| 30 – 27 | 1 | Data type = block trailer |
| 26 – 22 | Slot ID | Set by VME64 backplane |
| 21 – 0 | Total # of words in block of events | Number of 32 bit words in block |

Event Header Word Format

| Bits | Value | Comment |
|---------|----------------|------------------------------|
| 31 | 1 | This is a type defining word |
| 30 – 27 | 2 | Data type = event header |
| 26 – 22 | Slot ID | Set by VME64 backplane |
| 21 – 20 | Module type | 0=FADC250, etc. |
| 19 – 0 | Trigger number | ADC processing chip # |